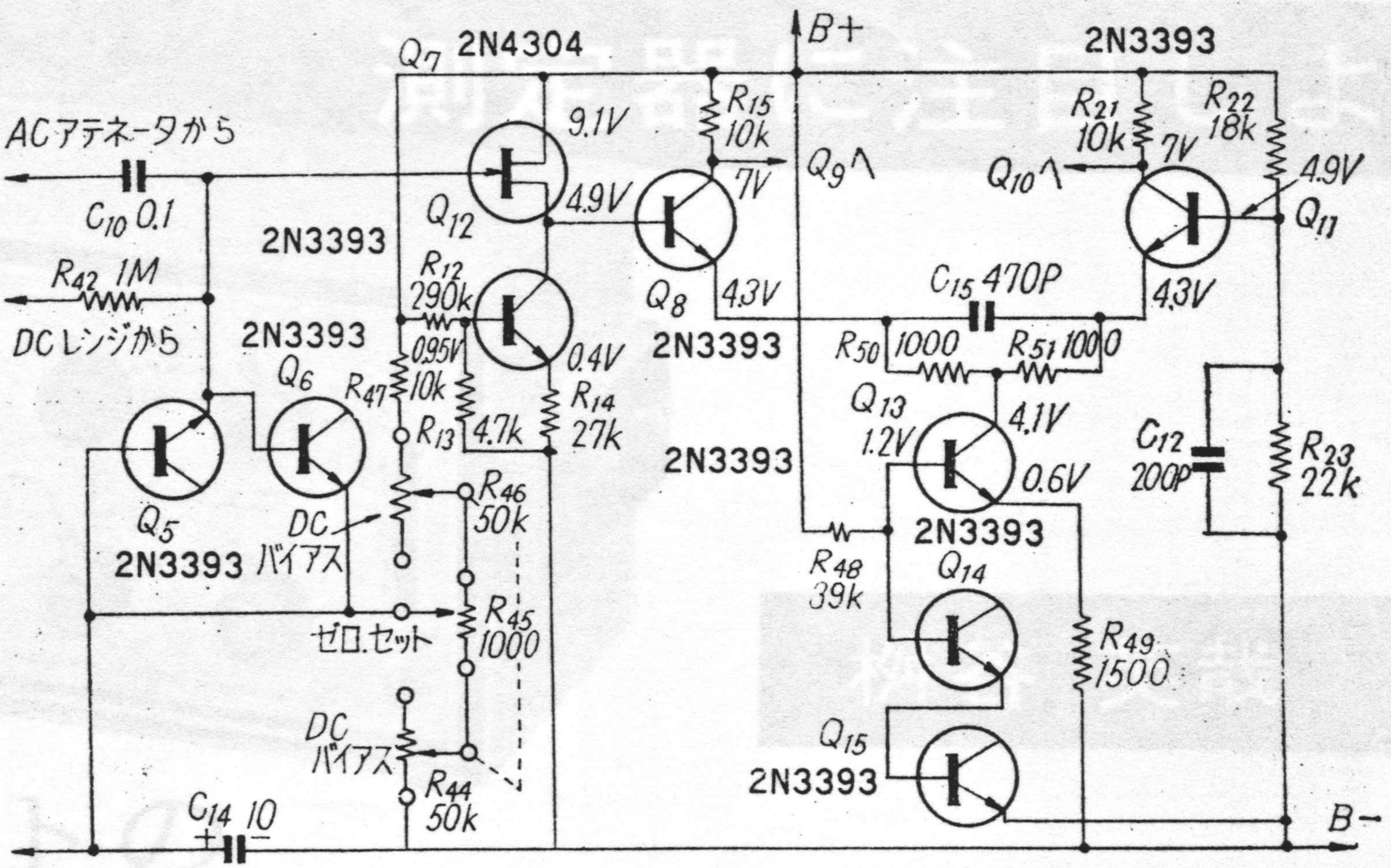

〈第6図〉 FETを初段とする増幅回路